

Transparent Memory Address Remapping

ABSTRACT OF THE DISCLOSURE

A subsystem that is able to address a second memory region using second identifiers initiates I/O requests directed to a device that is able to address a different, first memory region using first identifiers, which are represented using fewer bits than the first identifiers. Requests for memory are mapped at least once, for example from virtual to physical page numbers. A subset of the bits of the second identifiers may be preserved unchanged during remapping.